

**Amendments to the Claims:**

**Listing of Claims:**

- Claim 1 (currently amended) A method for manufacturing a polysilicon thin film transistor liquid crystal display, the polysilicon thin film liquid crystal display comprising:
- 5 a panel;  
a plurality of display cells, each display cell having at least a polysilicon thin film transistor;  
a timing control circuit for generating a timing signal; and  
10 a plurality of logic circuits for controlling operations of the display cells according to the timing signal;
- the method comprising:
- forming the plurality of display cells in the panel;  
forming the plurality of logic circuits in the panel, wherein at least two  
15 logic circuits have the same function; and  
determining a location in the panel for forming the timing control circuit so as to make differences among delay time intervals of the timing signals transmitted to different logic circuits with the same function less than 1000  $\mu$  s, and forming the timing control circuit accordingly,
- 20 wherein the timing signals are transmitted to different logic circuits with the same function by a plurality of transmitting lines, and differences between a product of an equivalent resistance value and an equivalent capacitance value of each transmitting line are less than 1000  $\mu$  s.
- 25 Claim 2 (cancelled)

Claim 3 (original) The method of claim 1 wherein the polysilicon thin film transistor further comprises a plurality of scan lines and data lines connected to the

display cells, and the plurality of logic circuits further comprise:  
a scan line driving circuit connected to the plurality of scan lines;  
a first data line driving circuit connected to the data lines of a first group; and  
a second data line driving circuit connected to the data lines of a second group,  
5           the data lines of the first group and the data lines of the second group being  
          arranged alternately.

Claim 4 (original) The method of claim 3 wherein the timing signal is respectively  
transmitted to the first data line driving circuit and to the second data line  
10           driving circuit by a first transmitting line and a second transmitting line,  
          differences between a product of an equivalent resistance value and an  
          equivalent capacitance value of the first transmitting line and a product of an  
          equivalent resistance value and an equivalent capacitance value of the second  
          transmitting line being less than  $1000 \mu s$ .

15           Claim 5 (original) The method of claim 1 wherein the polysilicon thin film liquid crystal  
          display further comprises an interface circuit for receiving and transmitting an  
          image signal such that the display cells operate according to the image signal.